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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10057205	FILING DATE 01/25/2002	CLASS 438	EXAMINER 2329 GEYER
**APPLICANTS: Quinnian Sion Sales Trd.			
**CONTINUING DATA VERIFIED: NONE SBF			
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** FOREIGN APPLICATIONS VERIFIED: UNITED KINGDOM 0126821.8 11/07/2001 0126821.8 SBF			
DO PUBLISH OR NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimant X yes <input checked="" type="checkbox"/> no <input type="checkbox"/>		ATTORNEY DOCKET NO	
US Office 15- day actions note X yes <input checked="" type="checkbox"/> no <input type="checkbox"/>		600001.02	
Verified and Acknowledged Examiners' initials SBF			
Title: Semiconductor package assembly and method for electrically isolating modules			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner PREPARED FOR ISSUE Application Examiner	
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